

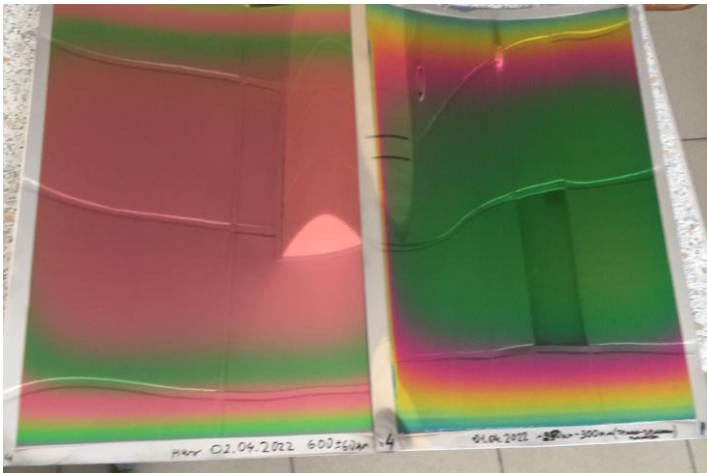
Thermal neutron GEM detector with VMM3/SRS readout



Temur Enik (JINR)

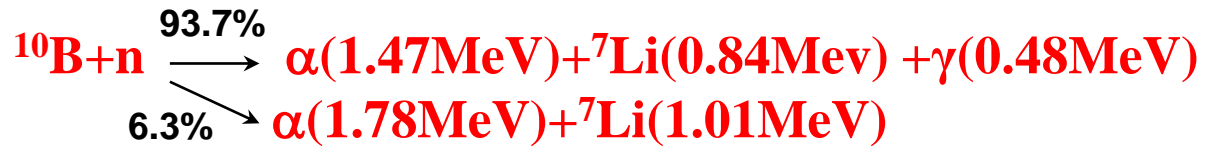
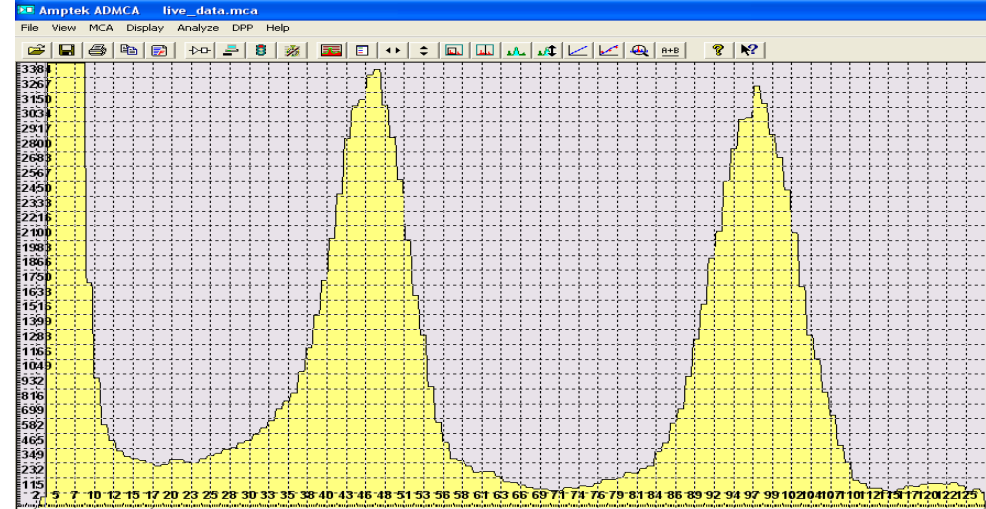
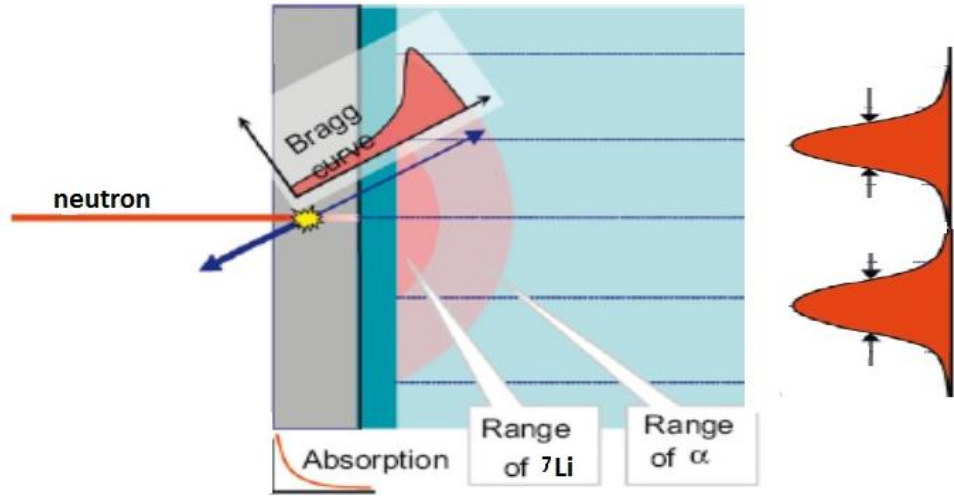
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Sharm El-Sheikh, Egypt

Facility for Coating in FLNP JINR



We propose use of B4C windows for the standard RD51 detectors in order to provide widely available neutron detectors

Neutron detector development in JINR



$$\sigma_a = 3835 \text{ barn}$$

Neutron detector:

- MWPC, A-C gap $h=6\text{mm}$, wire step $S=3\text{ mm}$, anode wire $D=20\ \mu\text{m}$
- B10 convertor thickness - $h=0.1\ \mu\text{m}$, sensitive area: $144 \times 48\ \text{mm}^2$
- Ar/ CO_2 , 70:30, gas flashing or filling up to $dP=3\ \text{Bar}$
- HV=(300-1500) V



Modern readout solutions – SRS+VMM3a hybrid by RD51 (CERN)

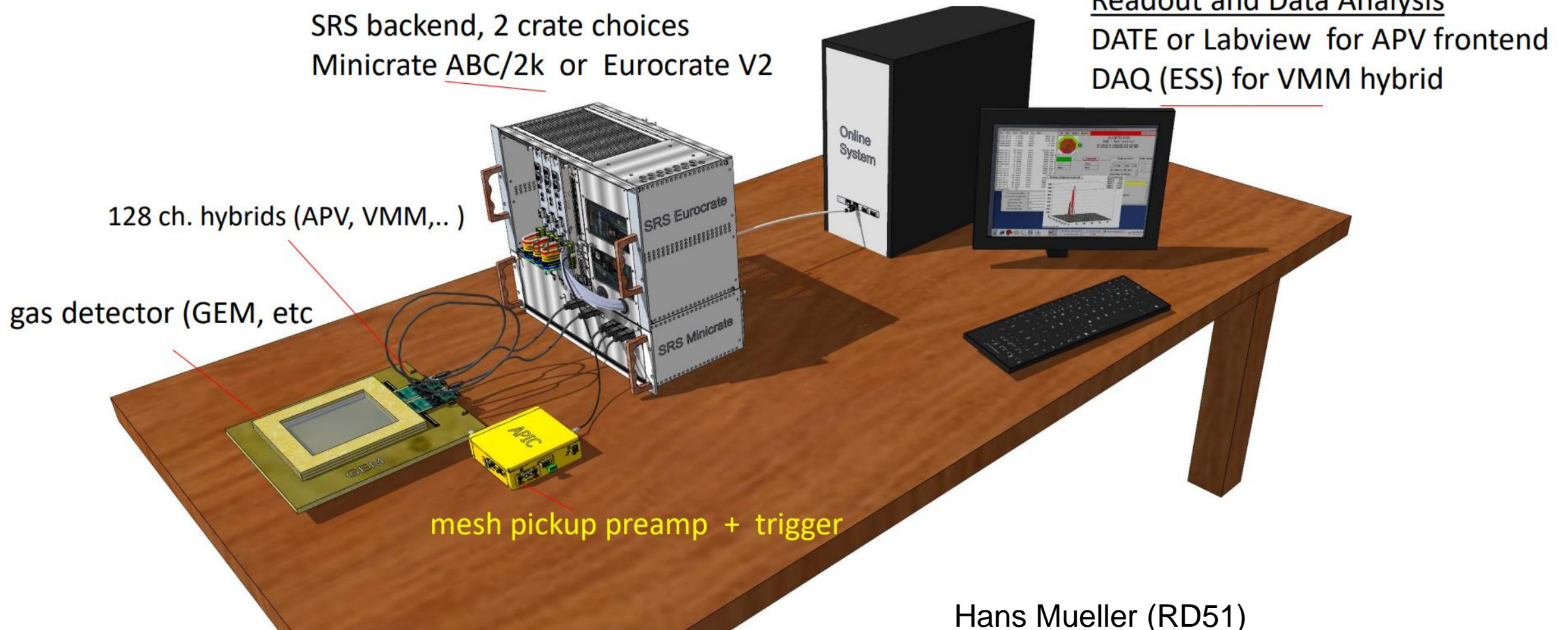
SRS backend, 2 crate choices
Minicrate ABC/2k or Eurocrate V2

Readout and Data Analysis
DATE or Labview for APV frontend
DAQ (ESS) for VMM hybrid

128 ch. hybrids (APV, VMM,..)

gas detector (GEM, etc)

mesh pickup preamp + trigger



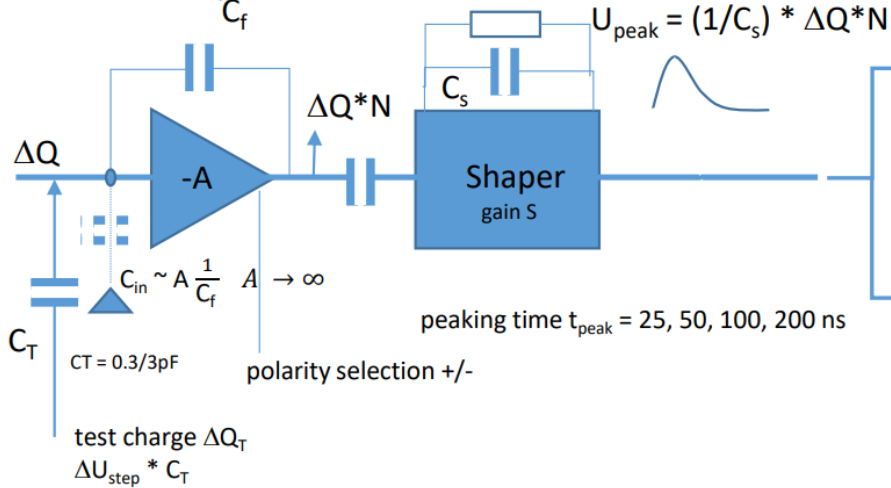
Hans Mueller (RD51)

Frontend - VMM3a

Chip configuration parameters (gain, threshold, ..) are programmable as bit-fields within one single 1728 bit stream

charge gain N

$$N = \frac{\Delta U}{\Delta Q} = \frac{1}{C_f} = 0.5, 1, 3, 4.5, 6, 9, 12, 16 \text{ mV/fC}^*$$



ANALOGUE

DIGITAL

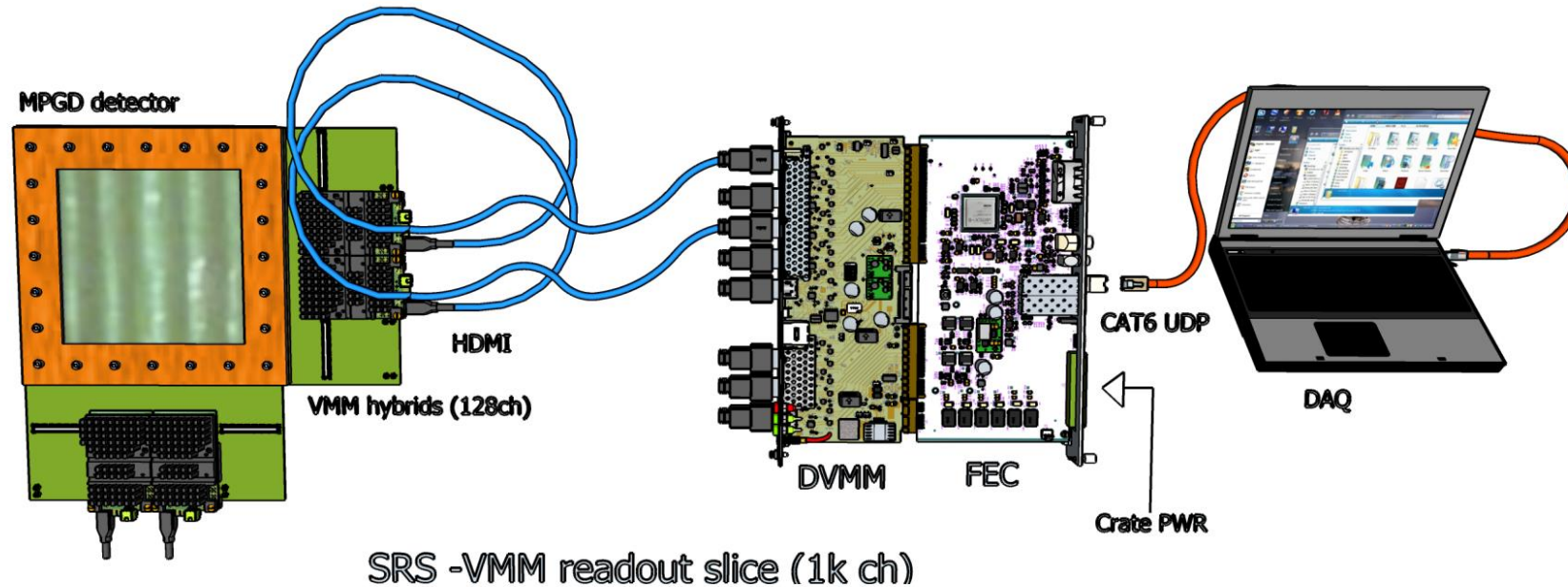
1st address over threshold

1 of 64 channels
per VMM ASIC

* with 1V max. peak amplitude:
 $\Delta Q_{\text{max}} = 2, 1, 0.33, 0.22, 0.17, 0.11, 0.08, 0.06 \text{ pC}$

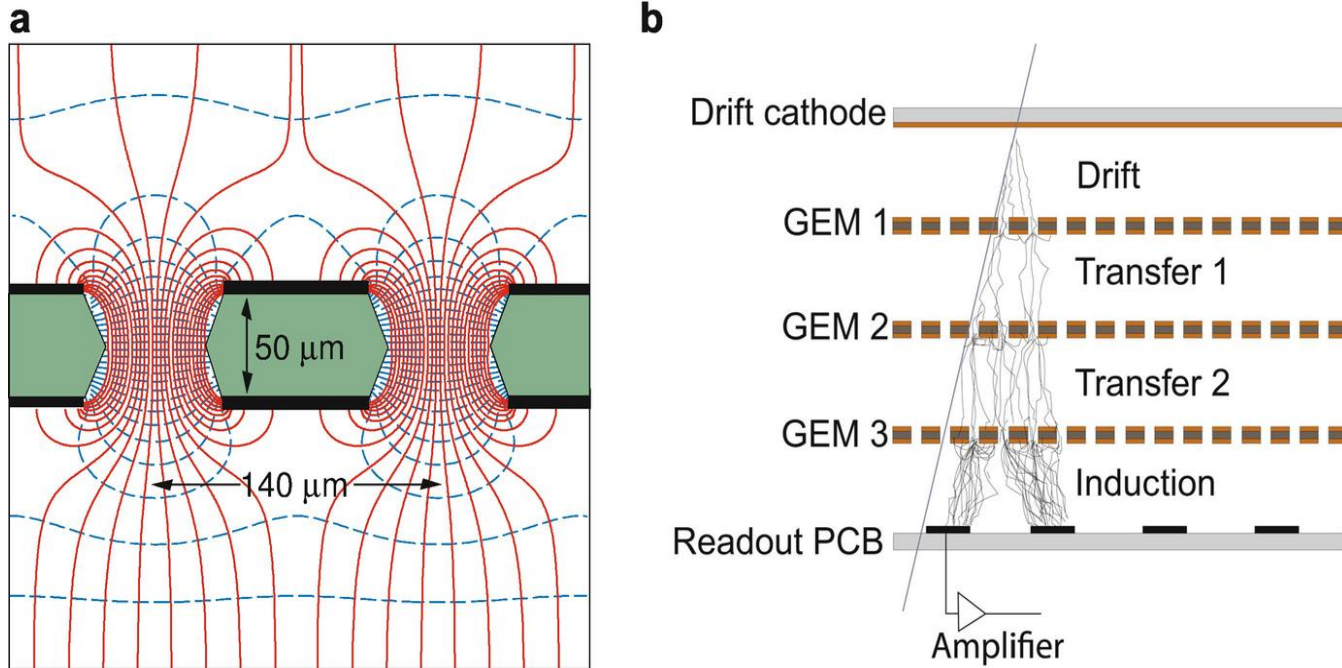
Scalable Readout System(SRS)

SRS with VMM frontend, so far implemented as a triggerless, scalable multichannel readout system for gas and photon detectors.



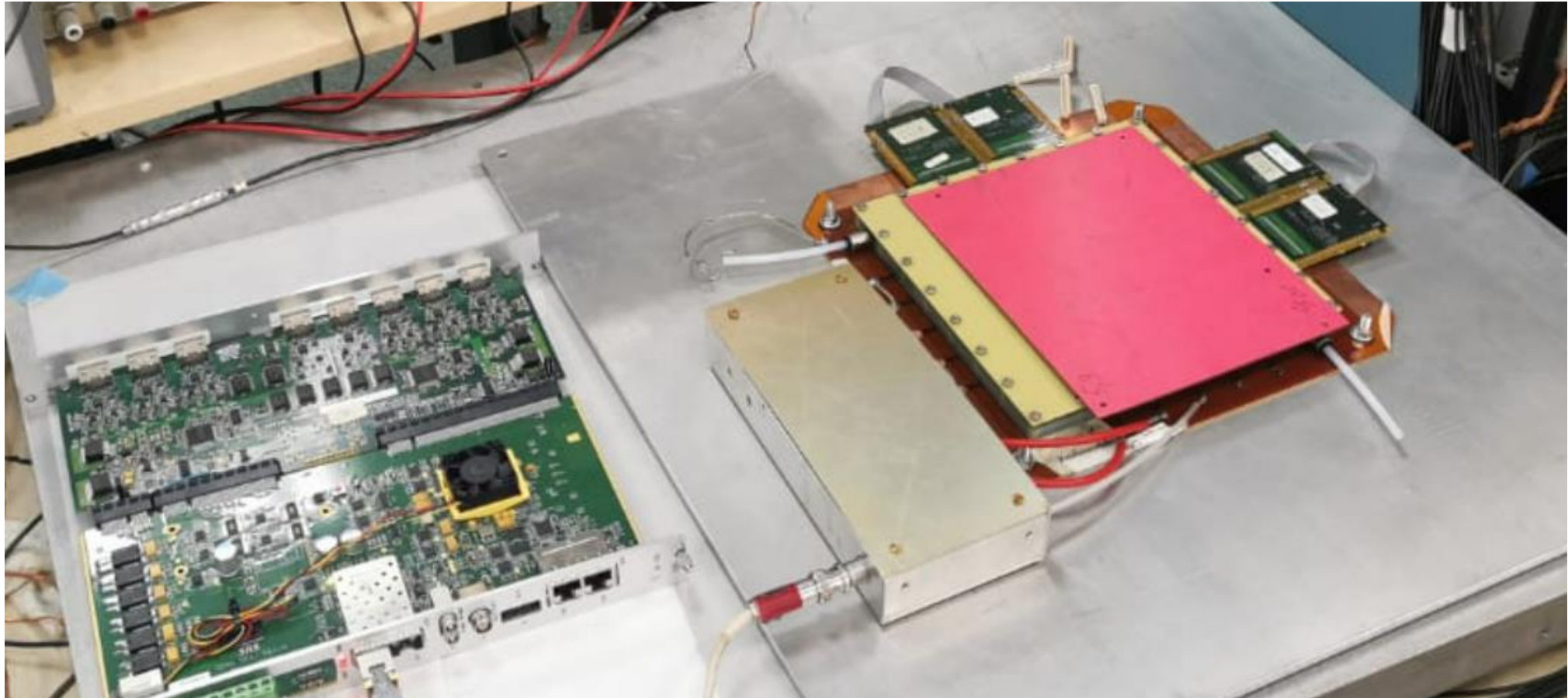
- 1 FEC for 8 VMM hybrids / 1024 detector channels connected via HDMI AD cables to DVMM.
- 2 FEC's with CTF (common clock) for 16 hybrids , 2048 ch.
- DVMM cards with octal HDMI connector including 70 W power for 16 VMM hybrids.
- Power / housing for 2 FECs and CTF via 1 Minicrate (not shown).
- 1 GB Ethernet /UDP uplink from FEC to Network via SFP+ jack, fiber or optical 1 GBE for network.
- ESS DAQ Software with Vmm Controls installed under Linux.

triple-GEM detector



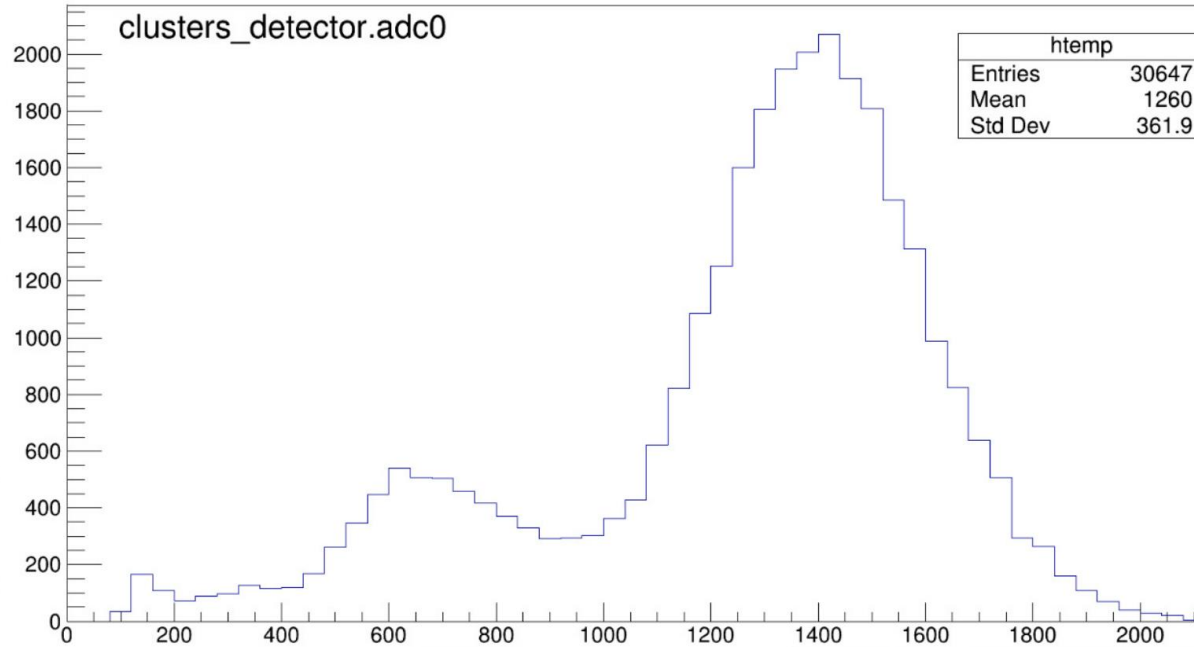
- a) Schematic view and typical dimensions of the hole structure in the GEM amplification cell. Electric field lines (solid) and equipotentials (dashed) are shown (Sauli and Sharma [1999](#));
- a) Schematic view of the triple-GEM detector. The original ionization occurs in the region labeled “Drift,” and the ionization electrons are drawn downward to the GEM foil. The amplified electrons emerging from the first GEM are drifted to the second GEM, where they again multiplied. One more GEM stage provides further amplification, and the output is collected at the readout plane (Sauli [2007](#)) (reproduced with permission from F. Sauli, 2012)

Detector and Reading System

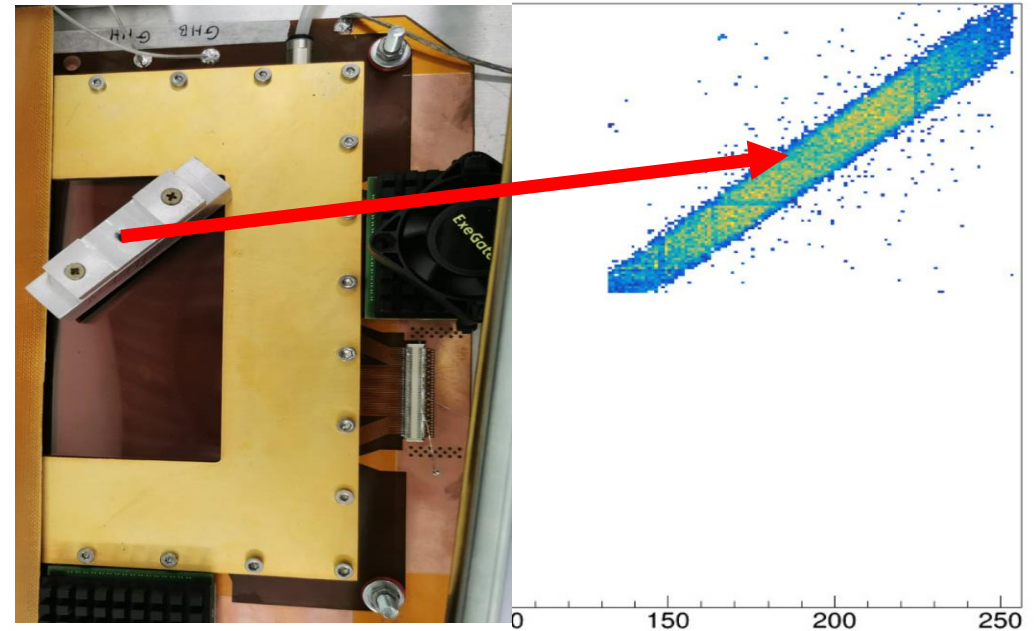


Testing basic configuration (standard tripleGEM+VMM3a hybrid+SRS) with ^{55}Fe

Amplitude spectrum of 5.9 keV ^{55}Fe



Coordinate distribution of illumination through a slit aluminum collimator



Replacement of the detector entrance window with a film with a coating B4C at JINR

thickness of the coating is about 60 nm





Conclusion

- We proved that gaseous detectors with B4C windows can be used as neutron detectors
- RD51 Collaboration (CERN) develops and provides standardized solutions for small-area gaseous detectors of ionizing radiation
- We propose use of B4C windows for the standard RD51 detectors in order to provide efficient and widely available thermal neutron detectors